

SURGE PROTECTION AND RESET CIRCUIT

ABSTRACT OF THE DISCLOSURE

A surge protection and reset circuit is provided for resetting a microprocessor to maintain a discharge lamp under a normal operating condition when the microprocessor is crashed owing to the cross talk or radiation effect in igniting a discharge lamp. The surge protection and reset circuit includes a ballast electrically connected to the discharge lamp for igniting the discharge lamp, a starting control circuit electrically connected to the ballast for triggering the ballast to ignite the discharge lamp and powering the ballast, a microprocessor electrically connected to the starting control circuit for initializing the starting control circuit when the microprocessor receives a lamp-state signal and a reset signal, and a reset circuit having an output terminal electrically connected to the microprocessor for providing the reset signal to reset the microprocessor when the reset circuit receives the lamp-state signal from a lamp-state terminal of the ballast.